

FIG. 1 (PRIOR ART)

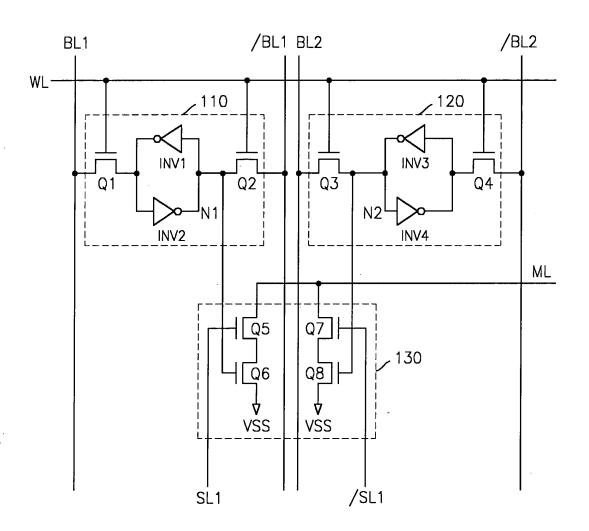
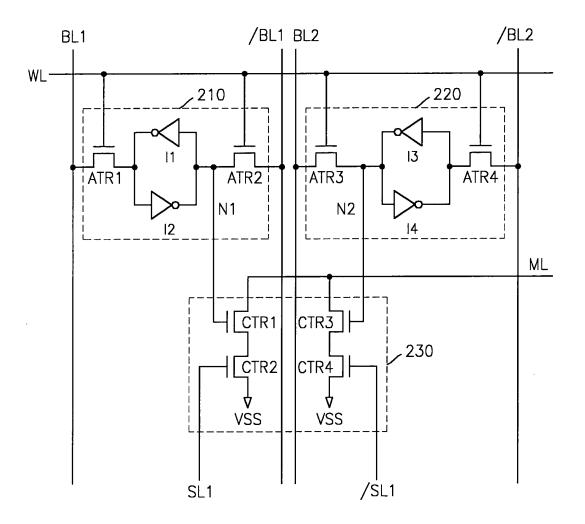
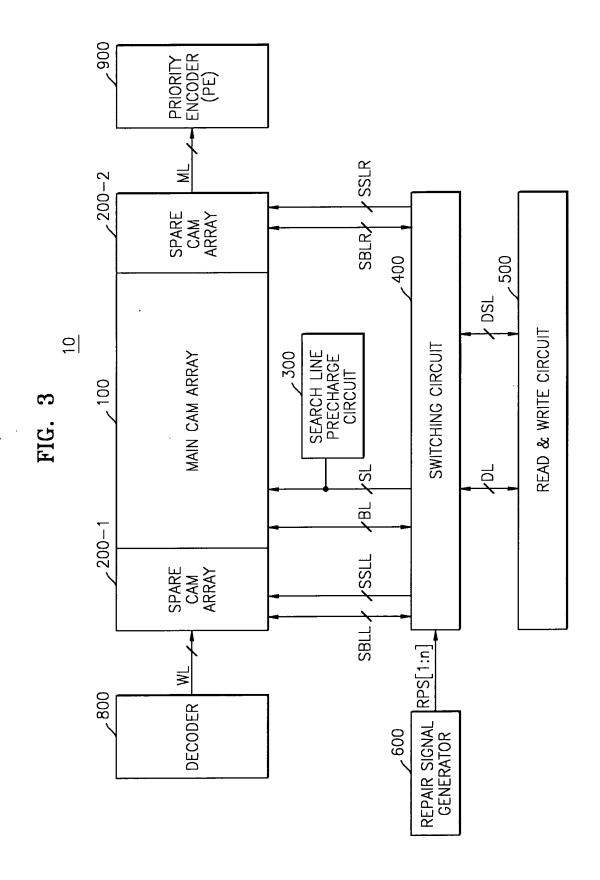
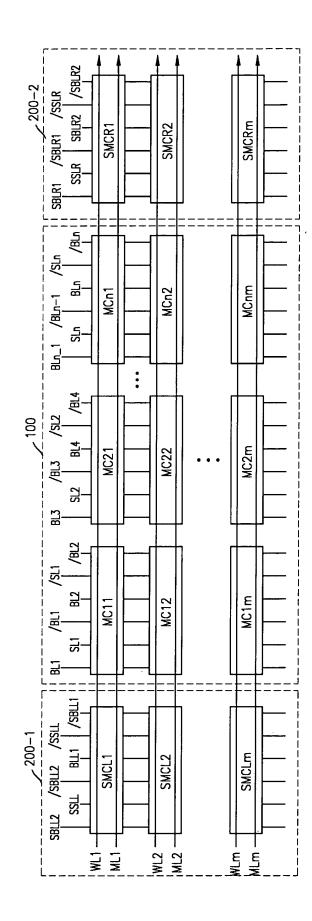
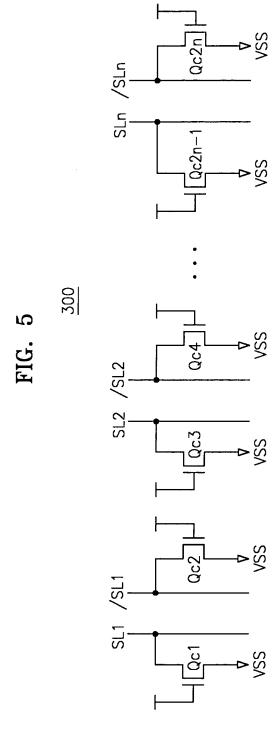


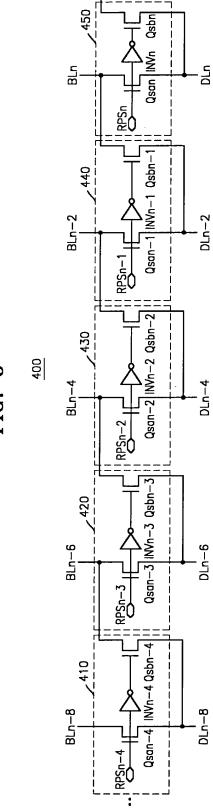
FIG. 2





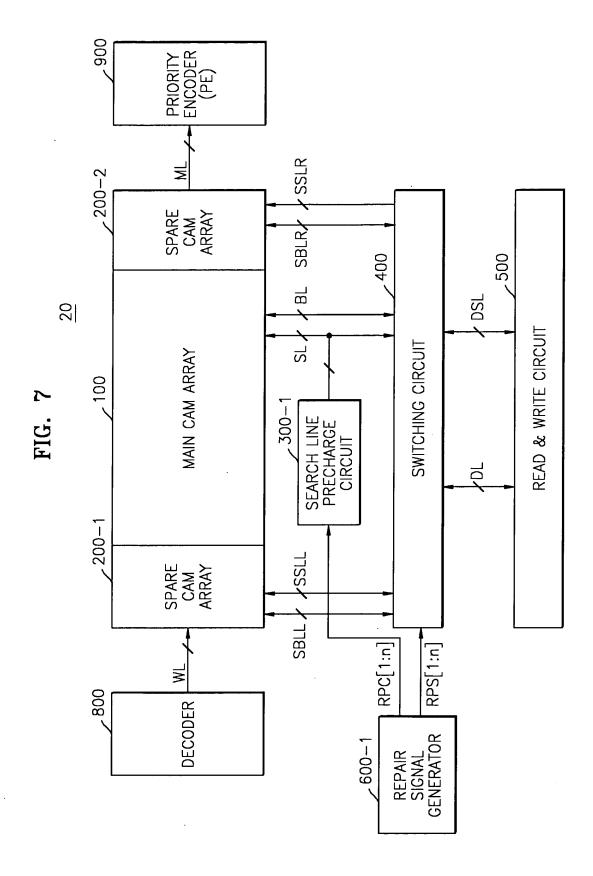


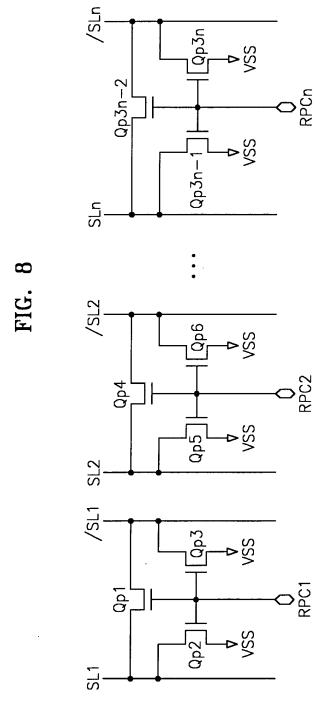


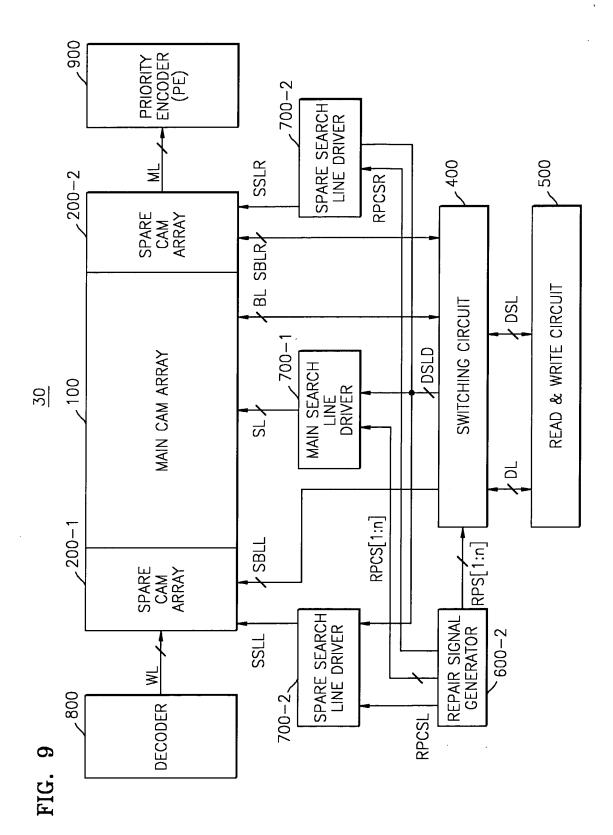


SBLR2

FIG. 6







→ RPCSn 730 NANDn SSLn DSLDn → RPCS2 /NAND2 .720 NAND2 DSLD2 → RPCS1 ,710 /DSLD1 NAND1 SSL1 DSLD1

FIG. 10A

FIG. 10B

